



**Princeton
Applied
Research**

AMETEK®

PARSTAT-AT

ADVANCED
Technology

ACCELERATED Discovery



PMC-1000 AT

Powered By

ASPIRE®

PRECISION MEETS AUTONOMY



Ethernet
Interface



122 aA Low
Current-Interface



3-slot
Chassis



Direct-To-Disk
Data Storage



8-wire Cell
Connections



Fully
Autonomous
Operation



24-bit
Resolution
150 nV / 30 fA



Thermistor
Input

PMC-1000 AT Specifications

System	Architecture	3-slot Ethernet chassis (1-slot per PMC-1000-AT or booster)
	Cell Connections	CE, WE, RE, SE plus 2x differential auxiliary inputs (8-wire)
Applied Voltage	Polarization / compliance	± 10 V / ± 12 V
	Voltage Scan / Pulse	Smooth scan, staircase, fast pulse
Applied Current	Maximum current	± 2 A (up to 20 A with internal booster, 30 A external booster)
	Current Scan / Pulse	Smooth scan, staircase, fast pulse
Voltage Inputs	Voltage Inputs	RE / SE, Aux-1, Aux-2 (synchronous)
	Ranges (maximum resolution)	10 V, 1 V (24-bit ADC 150 nV)
Current Input	Ranges (maximum resolution)	10 ranges , 2 A - 2 nA (24-bit ADC 30 fA)
EIS	Inputs	Main (RE/SE), Aux-1, Aux-2 (anode / cathode EIS)
	Frequency range	10 μ Hz to 1 MHz
Analog / Digital I/O	Temperature input	Thermistor
	Analog I/O	2x Analog inputs, 2x analog outputs
	Digital I/O	1x Digital inputs, 4x digital outputs – booster control, accessory
Options	Current Booster	Up to 2 boosters +6 V / -1 V, ± 10 A (± 20 A in parallel)
	Low Current Interface (LCI)	122 aA resolution
Aspire Software	Techniques - Energy, Electrochemistry, Corrosion	Including: CC, CV, CP, CR, LPR, Tafel, Linear / Staircase Voltammetry, Cyclic Voltammetry, Fast Pulse, SWV, DPV, PEIS, GEIS, Arbitrary Waveform, Ohmic Drop, IR compensation, Loops, Variables
	3rd Party Accessories	Integrated thermal chambers and data loggers

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